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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: James Holt; John So  
Assignee: Fairchild Semiconductor Corporation  
Title: Over-Voltage Protection Circuit  
Application No.: 10/027,708 Filing Date: December 19, 2001  
Examiner: Unknown Group Art Unit: Unknown  
Docket No.: M-11811 US



San Francisco, California  
April 19, 2002

COMMISSIONER FOR PATENTS  
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT  
UNDER 37 CFR § 1.97(b)

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the documents listed on the accompanying form PTO-1449 are called to the attention of the Examiner for the above patent application. Copies of these documents are enclosed.

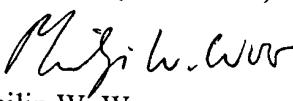
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Respectfully submitted,

  
Philip W. Woo  
Attorney for Applicants  
Reg. No. 39,880

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U.S. Department of Commerce, Patent and Trademark Office		Atty Docket No.	Serial No.
		M-11811 US	10/027708
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>O I P E</i>		Applicant(s)	
(Use several sheets if necessary)		James Holt, John So	
		Filing Date	Group
		December 19, 2001	Unknown

*APR 1 8 2002*  
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## U.S. Patent Documents

*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
CA	4,573,099	2/25/1986	Ganesan et al.	361	56	
CB	6,100,677	8/8/2000	Farrenkopf	323	285	
CC	5,381,061	1/10/1995	Davis	326	57	
CD	5,319,259	6/7/1994	Merrill	307	443	
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CF	5,161,183	11/3/1992	Maytum	379	412	
CG	5,479,119	12/26/1995	Tice et al.	327	74	
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## Foreign Patent Documents

Translation

	Document	Date	Country	Class	Subclass	Yes	No
CJ							

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CL	Maxim, "Notebook CPU Step-Down Controller for Intel Mobile Voltage Positioning (IMVP-II)," <a href="http://www.maxim-ic.com">www.maxim-ic.com</a> , February, 2001, MAX1718.
CM	Semtech, "Portable IMVP-IV™ Dual Phase Power Supply Controller," September, 2001, SC1476.
CN	Texas Instruments, "High-Density Synchronous Buck Converter Design Using TPS56xx Controllers", User's Guide, June 1999.
CO	Harris Corporation, "Advanced PWM and Dual Linear Power Control with Integrated ACPI Support Interface," December 1998, HIP6028.
CP	Di Bernardo et al., "Grazing, skipping and sliding: analysis of the non-smooth dynamics of the DC/DC buck converter," September 22, 1997, journal of publication unknown.

Examiner *Gary L. Larson* Date Considered *6-27-03*

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

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<i>CD</i>	AD 5,781,390	7/14/1998	Notaro et al.	361	84		
<i>CD</i>	AE 5,963,407	10/5/1999	Fragapane et al.	361	42		
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<i>CD</i>	AR	National Semiconductor, "LM2633, Advanced Two-Phase Synchronous Triple Regulator Controller for Notebook," <a href="http://www.national.com/pf/LM/LM2633.html">www.national.com/pf/LM/LM2633.html</a> , 2002.					
<i>CD</i>	AS	Linear Technology, "Dual High Efficiency, Low Noise, Synchronous Step-Down Switching Regulators," 1996, LTC 1538-AUX/LTC 1539.					
Examiner <i>Gary L. Carlton</i>		Date Considered 6-27-03					
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Foreign Patent Documents								
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		Document	Date	Country	Class	Subclass	Yes	No
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	BM							
	BN							
	BO							
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<i>CD</i>	BP	Linear Technology, "Wide Operating Range, No Rsense™ Step-Down Controller," 2001, LTC1778/LTC1778-1.						<i>RECEIVED TECHNICAL LIBRARY APR 6 2002</i>
<i>CD</i>	BQ	Analog Devices, "5-Bit Programmable 2-Phase Synchronous Buck Controller," 2001, ADP3160.						<i>RECEIVED TECHNICAL LIBRARY APR 6 2002</i>
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Examiner	<i>Cary L. Carlton</i>		Date Considered <i>6-27-03</i>					
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